REMARKS

This is in response to the Final Office Action of December 1, 2006. Claims 1-21, 23, and 28-32 are cancelled. Claims 22 and 24 are amended. New claims 33-35 are added.

By virtue of the cancellation of claim 14, it is respectfully submitted that the 35 USC § 112 rejection of this claim is moot.

Claim 22 was rejected under 35 U.S.C. § 103 over Bigjakkstaffa in view of Feierback and Cooper. In response to the 35 U.S.C § 103 prior art claim rejection of claim 22, Applicants have amended the claims to further distinguish over the cited art. Claim 22 was amended to clarify that the overclocking control module is disposed in the GPU, includes a clock controller and graphics pipeline stress tester, and is configured to evaluate overclocking parameters in response to a function call to the GPU. Support for this amendment is found in Figure 2 and in paragraphs [0020], [0022], and [0030]. Claim 24 was amended to include a driver limitation, and is supported by paragraph [0030]. New claims 33-35 are dependent from claim 22 and include limitations previously presented in other cancelled claims.

It is respectfully submitted that the cited art applied to claim 22 does not teach or suggest all of the limitations of claim 22. In particular, none of the cited references teaches the element of an "overclocking control module disposed in said GPU configured to evaluate overclocking parameters in response to a function call received by the GPU." Bigjakkstaffa is software based and relies upon a user manually juggling between several different software programs and using a visual test of the effect of the overclocking parameters. Feierback is directed to a different problem and moreover utilizes an "adaptive memory control program [that] is formed as part of the CPU 40." See, e.g., Feirback at paragraph [0013]. Cooper is also software based and for example relies upon a "software management tool" (paragraph [0027]) and executes a stress test on a software application executing on one of the host processors 110 (paragraph [0027]).

It is also noted that none of the cited references teaches or suggests an overclocking control module having such elements as: "a clock controller to control a GPU clock signal generator and a memory clock generator; and a graphics pipeline stress tester to implement a stress test that includes executing a graphics test sequence in said graphics pipeline for selected overclocking parameters and monitoring errors of said graphics pipeline."

In view of the foregoing amendments and remarks, it is respectfully submitted that the application is now in condition for allowance. The Examiner is invited to contact the undersigned if there are any residual issues that can be resolved through a telephone call.

The Commissioner is hereby authorized to charge any appropriate fees to Deposit Account No. 50-1283.

Respectfully submitted,
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Dated: April 2, 2007   By:

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